

Electronic Acknowledgement Receipt

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Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	Fractional-type Phase-Locked Loop circuit with compensation of phase errors
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		2110-111-03_20091104_OAR_20100114.pdf	172739 8f3b3213f7ab0930f48008c5c17e958347e9c5ee	yes	16
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Req. Reconsideration-After Non-Final Reject		1	1	
	Claims		2	10	
	Applicant Arguments/Remarks Made in an Amendment		11	16	
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	29928 2625989cb779c7648ea2b1d073e735febefda7ff	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			202667		

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New International Application Filed with the USPTO as a Receiving Office

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